

SURFACE MOUNT SILICON BI-DIRECTIONAL TRANSIENT VOLTAGE SUPPRESSOR



Central Semiconductor Corp.

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DESCRIPTION:

The CENTRAL SEMICONDUCTOR CFTVS3V3B is a low leakage, fast response, bi-directional TVS in the space saving SOD-882 surface mount package. This device is designed to protect sensitive equipment connected to high speed data lines against ESD damage.

MARKING CODE: 3

APPLICATIONS:

- Data line protection
- User interface protection
- Charging/power port protection

FEATURES:

- Space saving SOD-882 package
- · Low leakage current

MAXIMUM RATINGS: (T _A =25°C)	SYMBOL		UNITS
Peak Power Dissipation (8x20µs)	^Р РК	40	W
Electrical Fast Transient (IEC 61000-4-4) (5x50ns)	EFT	40	А
ESD Voltage (IEC 61000-4-2, Air)	V _{ESD}	30	kV
ESD Voltage (IEC 61000-4-2, Contact)	V _{ESD}	30	kV
Operating Junction Temperature	ТЈ	-55 to +125	°C
Storage Temperature	T _{stg}	-55 to +150	°C

ELECTRICAL CHARACTERISTICS: (T_A=25°C)

Maximum Reverse Stand-off Voltage V _{RWM}	Minimum Breakdown Voltage V _{BR} @ ^I T	Test Current I _T	Maximum Reverse Leakage Current I _R @ V _{RWM}	Clarr Volt (8x2	mum nping tage 0µs) D Ipp	TLP CI	,	Typical Dynamic Resistance (Note 1) R _{DYN}	Maximum Junction Capacitance @ 0V Bias CJ
v	v	μΑ	μΑ	v	А	v	А	Ω	pF
3.3	3.5	2.0	0.5	6.5	1.0	6.0	4.0	0.25	10
3.3	3.5	2.0	0.5	8.0	5.0	7.0	8.0	0.25	10

Note 1: Transmission Line Pulse (TLP) conditions: $Z_0=50\Omega$, tp=100ns

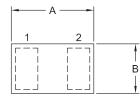
R8 (28-July 2015)



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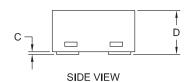
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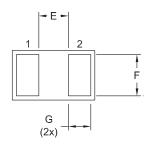
SOD-882 CASE - MECHANICAL OUTLINE



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TOP VIEW





BOTTOM VIEW R2

DIMENSIONS					
	INCHES		MILLIMETERS		
SYMBOL	MIN	MAX	MIN	MAX	
А	0.037	0.042	0.95	1.05	
В	0.022	0.026	0.55	0.65	
С	0.000	0.002	0.00	0.05	
D	0.017	0.022	0.45	0.55	
E	0.014		0.36		
F	0.017	0.022	0.45	0.55	
G	0.008	0.013	0.22	0.32	
SOD-882 (REV:R2)					

PIN CONFIGURATION



LEAD CODE: 1) Anode 1 2) Anode 2

MARKING CODE: 3

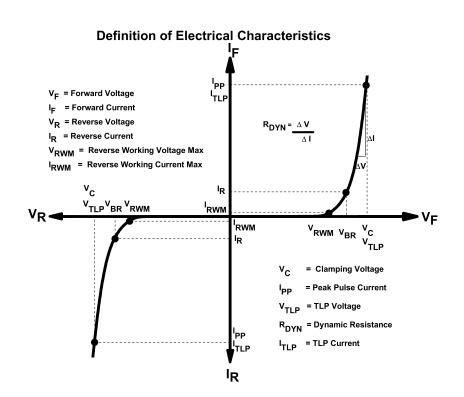
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BI-DIRECTIONAL TRANSIENT VOLTAGE SUPPRESSOR

Reverse Voltage Leakage Current 5.4 100 I_R, Reverse Current (nA) V_{BR}, Breakdown Voltage (Volts) 5.3 10 5.2 I_R=1 mA 5.1 |- -25 0.1 |- 25 V_R=3.3V 25 75 ò 50 75 100 125 50 100 T_A, Ambient Temperature (°C) T_A, Ambient Temperature (°C) Peak Clamping Voltage (V_C) vs Peak Pulse Current (I_{PP}) Capacitance 10 6.0 -V_C, Clamping Voltage (Volts) C, Capacitance (pF) 8 5.5 -7 6 -5 -5.0 -4 3 4.5 2 -T_A=25°C _____ f=1.0MHz -1 -0+ 4.0 -3

TYPICAL ELECTRICAL CHARACTERISTICS

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IPP, Peak Pulse Current (Amps)

125

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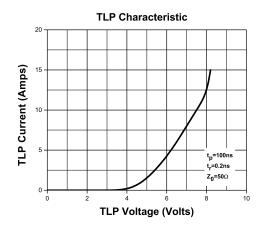
V_R, Reverse Voltage (Volts)



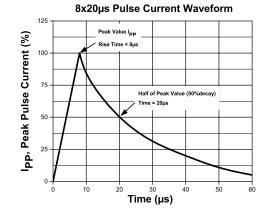
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TYPICAL ELECTRICAL CHARACTERISTICS



Ipp Current Derating in Percentage (%) **Power & Current Derating** 125 Р_{РК}, PeakPower Derating or 100 75 50 · 25 0+ 25 50 75 100 125 150 T_A, Ambient Temperature (°C)

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OUTSTANDING SUPPORT AND SUPERIOR SERVICES



PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- Inventory bonding
- Consolidated shipping options

- Custom bar coding for shipments
- Custom product packing

DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free quick ship samples (2nd day air)
- Online technical data and parametric search
- SPICE models
- Custom electrical curves
- Environmental regulation compliance
- Customer specific screening
- Up-screening capabilities

- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- Application and design sample kits
- Custom product and package development

CONTACT US

Corporate Headquarters & Customer Support Team

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