

Important notice

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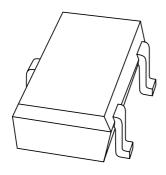
If you have any questions related to the data sheet, please contact our nearest sales office via e-mail or telephone (details via **salesaddresses@nexperia.com**). Thank you for your cooperation and understanding,

Kind regards,

Team Nexperia

DISCRETE SEMICONDUCTORS

DATA SHEET



PMSTA42; PMSTA43 NPN high-voltage transistors

Product data sheet Supersedes data of 1997 Jun 19 1999 May 21



NPN high-voltage transistors

PMSTA42; PMSTA43

FEATURES

- High current (max. 500 mA)
- High voltage (max. 200 V).

APPLICATIONS

• High-voltage switching in telephony applications.

DESCRIPTION

NPN high-voltage transistor in a SOT323 plastic package. PNP complements: PMSTA92 and PMSTA93.

MARKING

TYPE NUMBER	MARKING CODE ⁽¹⁾
PMSTA42	*1D
PMSTA43	*1E

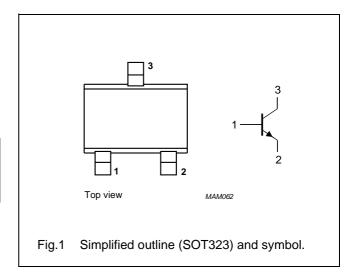
Note

1. * = -: Made in Hong Kong.

* = t : Made in Malaysia.

PINNING

PIN	DESCRIPTION
1	base
2	emitter
3	collector



LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter			
	PMSTA42		_	300	V
	PMSTA43		_	200	V
V _{CEO}	collector-emitter voltage	open base			
	PMSTA42		_	300	V
	PMSTA43		_	200	V
V _{EBO}	emitter-base voltage	open collector	_	6	V
I _C	collector current (DC)		_	100	mA
I _{CM}	peak collector current		-	200	mA
I _{BM}	peak base current		_	100	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C; note 1	-	200	mW
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		-	150	°C
T _{amb}	operating ambient temperature		-65	+150	°C

Note

1. Transistor mounted on an FR4 printed-circuit board.

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NPN high-voltage transistors

PMSTA42; PMSTA43

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th j-a}	thermal resistance from junction to ambient	note 1	625	K/W

Note

1. Transistor mounted on an FR4 printed-circuit board.

CHARACTERISTICS

 T_{amb} = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I _{CBO}	collector cut-off current				
	PMSTA42	I _E = 0; V _{CB} = 200 V	_	100	nA
	PMSTA43	$I_E = 0; V_{CB} = 200 \text{ V}$ $I_E = 0; V_{CB} = 160 \text{ V}$	_	100	nA
I _{EBO}	emitter cut-off current				
	PMSTA42	I _C = 0; V _{EB} = 6 V	_	100	nA
	PMSTA43	I _C = 0; V _{EB} = 4 V	_	100	nA
h _{FE}	DC current gain	I _C = 1 mA; V _{CE} = 10 V	25	_	
		I _C = 10 mA; V _{CE} = 10 V	40	_	
		I _C = 30 mA; V _{CE} = 10 V; note 1	40	_	
V _{CEsat}	collector-emitter saturation voltage	$I_C = 20 \text{ mA}; I_B = 2 \text{ mA}$	_	500	mV
C _{re}	feedback capacitance	$I_C = i_c = 0$; $V_{CB} = 20 \text{ V}$; $f = 1 \text{ MHz}$			
	PMSTA42		_	3	pF
	PMSTA43		_	4	pF
f _T	transition frequency	$I_C = 10 \text{ mA}; V_{CE} = 20 \text{ V}; f = 100 \text{ MHz}$	50	_	MHz

Note

1. Pulse test: $t_p \le 300~\mu s;~\delta \le 0.02.$

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NPN high-voltage transistors

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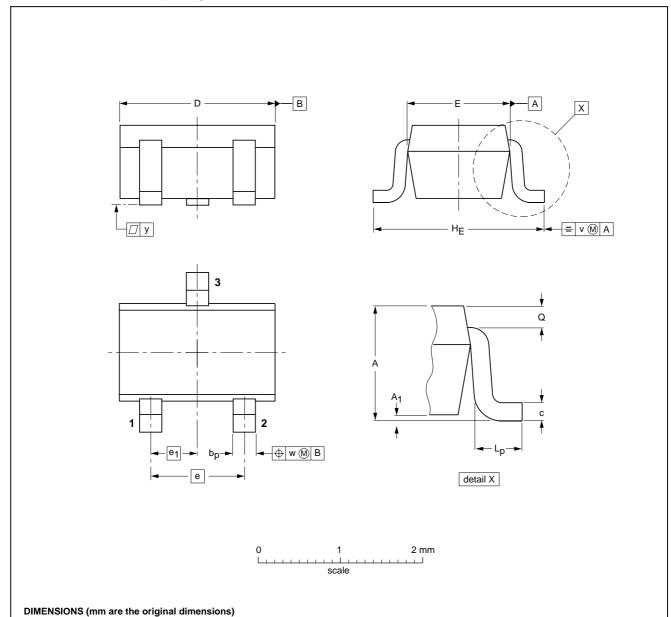
PACKAGE OUTLINE

UNIT

mm

Plastic surface mounted package; 3 leads

SOT323



OUTLINE		REFERENCES			EUROPEAN	ISSUE DATE
VERSION	IEC	JEDEC	EIAJ		PROJECTION	ISSUE DATE
SOT323			SC-70			97-02-28

e₁

0.65

1.3

 ${\sf H}_{\sf E}$

2.0

 $L_{\mathbf{p}}$

0.45

Q

0.2

0.2

Ε

1.35

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 $b_{\mathbf{p}}$

0.25

0.10

2.2

max

0.1

8.0

NPN high-voltage transistors

PMSTA42; PMSTA43

DATA SHEET STATUS

DOCUMENT STATUS ⁽¹⁾	PRODUCT STATUS ⁽²⁾	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

Notes

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NXP Semiconductors

Customer notification

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Contact information

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